

## **AMENDMENTS TO DRAWINGS**

The attached sheets of drawings include changes to Figures 7-9. These sheets, which include Figures 7-9, replace the original sheets including Figures 7-9.

In Figure 7, the text in converters 280 and 290 has been changed to map "1" to "-1."

In Figure 8, converters 280 and 290 have been changed to extract bits (L-1) and (L-2) respectively.

In Figure 9, the text of the fourth method element has been changed to read "EXTRACT BITS (L-1) and (L-2)," and the text of the fifth method element has been changed to read "GENERATE I AND Q VALUES FROM BITS (L-1) and (L-1)  $\ominus$  (L-2)."

**REMARKS**

Specification paragraphs [0046], [0047], [0048], and [0049] have been amended to correct the typographical errors. No new matter has been introduced into the application by these amendments.

**Double Patenting Rejections**

Claims 1 and 7 are rejected under the judicially created doctrine of non-statutory obviousness-type double patenting as being unpatentable over claims 6 and 15 of U.S. Patent No. 6,026,117. A Terminal Disclaimer is submitted herewith. Accordingly, the withdrawal of the non-statutory obviousness-type double patenting rejections is respectfully requested.

The Examiner is also thanked for noting the allowable matter in this application.

**Applicant:** Ozlutar  
**Application No.:** 10/777,369

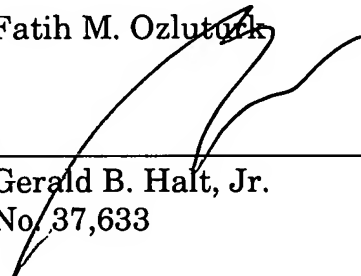
**Conclusion**

If the Examiner believes that any additional minor formal matters need to be addressed in order to place this application in condition for allowance, or that a telephone interview will help to materially advance the prosecution of this application, the Examiner is invited to contact the undersigned by telephone at the Examiner's convenience.

In view of the foregoing amendments and remarks, Applicant respectfully submits that the present application, including claims 1-12, is in condition for allowance and a notice to that effect is respectfully requested.

Respectfully submitted,

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